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Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> Date Submitted: January 18, 2005 (use as many sheets as necessary)		<b>Complete if Known</b>	
1 of 3		Application Number	10/687,186
		Filing Date	10/16/2003
		First Named Inventor	Sergey D. Lopatin
		Group Art Unit	2813
		Examiner Name	Thanh T. Nguyen
		Attorney Docket Number	039153-0484 (G1190)

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
TW	A1	2002/0137332	A1	Paranjpe et al.	09-26-2002	
	A2	2002/0109233	A1	Farrar	08-15-2002	
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Examiner Signature	<i>Tw</i>	Date Considered	4/14/05
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<sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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				Examiner Name	Thanh T. Nguyen
Sheet	2	of	3	Attorney Docket Number	039153-0484 (G1190)

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
TW	A58	EP	1 094 515	A2		04-25-2001		
	A59	EP	1 039 531	A2		09-27-2000		
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>6</sup>
TW	A62	4.7.3 General Reliability Issues Associated with IC Interconnects; Silicon Processing for the VLSI Era; Vol. II, pp. 264-265.			
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TN	A64	Dong Joon Kim et al, "New Method to Prepare W-B <sup>+</sup> -N Ternary Barrier to Cu diffusion by Implanting BF <sub>2</sub> <sup>+</sup> Ions Into W-N Thin Film," J. Vac. Sci. Technol. B 17(4), July/Aug, 1999, pp. 1598-1601.	
	A65	W. F. McArthur et al., "Structural and Electrical Characterization of Si-Implanted Tin as a Diffusion Barrier for Cu Metallization," Mat. Res. Soc. Symp. Proc. Vol. 391, 1995, pp. 327-332.	
	A66	PCT International Search Report, International Application No. PCT/US 02/32605, International Filing Date 11/10/2002 (7 pages).	
TN	A67	PCT International search Report, International Application No. PCT/US 02/32554, International Filing Date 11/10/2002 (5 pages).	

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